

U.S. Department of Commerce, Patent and Trademark INFORMATION DISCLOSURE STATEMENT BY APPLICANT		Atty. Docket No. SNDK.006USS	Application No. 09/129,675
JUL 10 2006 Use several sheets if necessary)		Applicant(s) Harari et al.	Conf. No. 4949
(Form PTO-1449)		Filing Date August 5, 1998	Art Group 2824

**U.S. Patent Documents**

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
AT	1	4,890,259	12/26/89	Simko	365	185.03	

**U.S. Published Patent Application Documents**

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate

**Foreign Patent Documents**

							Translation
		Document	Date	Country	Class	Subclass	Yes

**OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)**

AT	2	Prior Art Cited by STMicro re U.S. Patent 5,172,338 – U.S. Patent and Application, 34 pages.
AT	3	Prior Art Cited by STMicro re U.S. Patent 5,172,338 – Foreign Patents/Applications; Publications; Data Sheets and Web Pages, 27 pages.
AT	4	Public Version of Complainant SanDisk Corporation's Reply to Respondents' Post-Hearing Brief, Investigation No. 337-TA-526, October 7, 2005, pp 81-128.
AT	5	Public Version of Complainant SanDisk Corporation's Post-Hearing Brief, Investigation No. 337-TA-526, October 7, 2005, pp. 99-134.

Examiner /Andrew Tran/	Date Considered 12/22/2006
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*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
AT	1	4,698,750	10/6/87	Wilkie et al.	365	185.04	
	2	5,222,046	6/22/93	Kreifels et al.	365	185.22	
	3	5,768,191	6/16/98	Choi et al.	365	185.22	
	4	6,118,703	9/12/00	Hamaguchi	365	185.22	
	5	6,496,446	12/17/02	Suzuki	365	238.5	
AT	6	4,989,179	1/29/91	Simko	365	185.03	

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## Foreign Patent Documents

		Document	Date	Country	Class	Subclass	Translation
AT	7	JP2-10596	1/16/90	Japan	G11C	16/06	
	8	JP3-286497	12/17/91	Japan	G11C	16/06	Abstract
	9	JP4-82090	3/16/92	Japan	G11C	16/06	Abstract
	10	JP9-106689	4/22/97	Japan	G11C	16/06	Abstract
AT	11	JP11-176185	7/2/99	Japan	G11C	17/18	Abstract

## OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)


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(Use several sheets if necessary)			Eliyahou Harari et al.			4949	
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			August 5, 1998			2824	
<b>U.S. Patent Documents</b>							
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
AT	1	4,752,871	6/21/1988	Sparks et al.	365	185.11	
AT	2	5,136,541	8/4/1992	Arakawa	365	185.23	
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		Document	Date	Country	Class	Subclass	Translation
AT	3	JP S 59-121696	7/13/1984	Japan	G11C	17/00	X
AT	4	JP H62-6493	1/13/1987	Japan	G11C	17/00	X
<b>OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)</b>							
AT	5	S. Mehrotra et al., "Serial 9Mb Flash EEPROM for Solid State Disk Applications," 1992 Symposium on VLSI Circuits Digest of Technical Papers, IEEE, pp. 24-25.					
AT	6	David A. Rich, "A Survey of Multivalued Memories," IEEE Transaction of Computers, Vol. C-35, No. 2, February, 1986, pp. 99-106.					
AT	7	Clinton Kuo et al., "An 80 ns 32K EEPROM Using the FETMOS Cell," IEEE Journal of Solid-State Circuits, Vol. SC-17, No. 5, October, 1982, pp. 821-827.					
AT	8	Electronic Industries Association, JEDEC Standard, "Configurations for Solid State Memories, JEDEC Standard No. 21-B (Revision of JESD21-A and JESD21-A-1), CS-0336, 337-TA-560, December, 1988, pp. i-36, 39-147.					
AT	9	"Expert Report of Dr. Richard Pashley Regarding the Invalidity of U.S. Patent No. 5,172,338 and U.S. Patent No. 5,991,517," United States International Trade Commission, Investigation No. 337-TA-560, September 7, 2006, pp. i-387.					
AT	10	"Rebuttal Expert Report of G.R. Mohan Rao, Ph.D. Regarding Validity and Enforceability of U.S. Patent Nos. 5,172,338 and 5,991,517," United States International Trade Commission, Investigation No. 337-TA-560, September 26, 2006, pp. i-245, Exhibit List, 6 pages.					
AT	11	"Addendum to the Rebuttal Expert Report of G.R. Mohan Rao, Ph.D. Regarding Validity and Enforceability of U.S. Patent Nos. 5,172,338 and 5,991,517," United States International Trade Commission, Investigation No. 337-TA-560, September 28, 2006, 5 pages.					
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